



St. Thomas' College of Engineering & Technology

4 D.H. ROAD, Kolkata-700023

NEWSLETTER



Darpan
(The Image of Achievements)

DEPARTMENT OF
ELECTRONICS & COMMUNICATION ENGINEERING

-: July to September :-

VOLUME ECX - EDITION I, OCTOBER 17, 2024

For Further Information Contact: prasun.chowdhury@stcet.ac.in

For Feedback Contact: sudipta.dutta@stcet.ac.in

Have a visit to our website:

www.stcet.ac.in



St. Thomas' College of Engineering and Technology

4, D. H. Road, Kidderpore, Kolkata – 700023

Department of Electronics & Communication Engineering

Darpan

(The Image of Achievements)

Volume: ECX - Edition I, 17th October, 2024

Editors:

1. Mr. Jishnu Bhadra
B.Tech 4th year,
2. Ms. Sumana Manna
B.Tech 4th year,
3. Mr. Kousik Sarkar
B.Tech 3rd year,
4. Miss. Rupsa Chakraborty
B.Tech 3rd year
5. Mr. Adrishikhar Chowdhury
B.Tech 2nd year
6. Ms. Adrika Sarkar
B.Tech 2nd year
7. Ms. Mohar Chakraborty
B.Tech 1st year
8. Mr. Raj Nandi
B.Tech 1st year

Faculty in-charge :

1. Sudipta Dutta,
Assistant Professor
2. Prashnatit Pal,
Assistant Professor

For

Further Information Contact:

prasun.chowdhury@stcet.ac.in

For Feedback Contact:

sudipta.dutta@stcet.ac.in

Vision of the Department

To build a strong teaching and research environment to cater to the manpower needs in Industrial and Academic domains of the rapidly growing Electronics and Communication Engineering.

Mission of the Department

- To produce certified industry-ready professional in Electronics and Communication Engineering, through innovative educational programs incorporating laboratory practices and project-based teaching-learning processes, in a modern environment.
- To create knowledge base of advanced technologies through research in the area of Electronics and Communication, for competitive and sustainable development of the country.
- To groom the department as a learning centre to inculcate advancement of technology in Electronics and Communication Engineering with social values and environmental awareness.

Program Educational Objectives (PEO)

- Possess design skills and core proficiency in Electronics & Communication Engineering for employment in relevant industries.
- Possess strong core and advanced knowledge Electronics & Communication Engineering to pursue higher studies.
- Exhibit leadership, ethical values, and social commitment to contribute in academia, industry and/or entrepreneurial ventures, both nationally and globally.



St. Thomas' College of Engineering and Technology

4, D. H. Road, Kidderpore, Kolkata – 700023

Department of Electronics & Communication Engineering

Recent Technology Trend

Edge Computing for Improved Reliability

Edge computing is a distributed computing paradigm that brings data processing, storage, and analytics closer to the physical location where data is generated, rather than relying solely on centralized cloud servers. By minimizing the distance between data sources and processing units, edge computing significantly reduces latency, enhances real-time responsiveness, and improves reliability—making it essential for applications such as autonomous systems, smart manufacturing, IoT sensor networks, and augmented/virtual reality. Additionally, researchers are exploring energy-efficient algorithms, AI-enabled edge analytics, and scalable edge networks capable of supporting massive IoT deployments and 5G/6G access technologies. As edge computing continues to evolve, ongoing studies aim to overcome challenges related to interoperability, fault tolerance, and seamless mobility, ultimately leading to more autonomous, resilient, and intelligent edge ecosystems.

Dr. Prasun Chowdhury

HOD, Department of Electronics and Communication Engineering

Departmental Milestones

STUDENTS' ACHIEVEMENTS

UNIVERSITY RESULT:

- Top three Final year students who completed Degree course.

Name	DGPA
ANKITA SINGH	9.42
SUBHADIPOA DEY	9.33
SREEMOYEE GHOSH	9.28

- Top three performers in 3rd Year (6th Semester) Exam

Name	YGPA
SOUMIK PAL	9.17
RAJORITA ROY	9.16
PABONI BHATTACHARYYA	9.04

- Top three performers in 2nd Year (4th Semester) Exam

Name	YGPA
RUPSA SAHA CHOWDHURY	8.84
AGNIK GHOSH	8.55
SUPRATIM BANERJEE	8.45



St. Thomas' College of Engineering and Technology

4, D. H. Road, Kidderpore, Kolkata – 700023

Department of Electronics & Communication Engineering

CELEBRATION OF NATIONAL SPACE DAY

An **Exhibition & Model Making Competition** was organized as part of **National Space Day Celebration**, organized by the IEEE Student Branch in collaboration with IIC and Research & Development Cell, St. Thomas' College of Engineering and Technology, Kolkata, held on August 23, 2024.

Tirupati Banerjee, Parna Das, Mohar Chakrabarty students of **1st Year**, have been awarded certificate of participation in this Exhibition & Model Making Competition.

INDUCTION PROGRAMME 2024-2025

Parna Das, student of **1st Year**, has secured **2nd** position in **painting competition**, during the **Induction programme 2024-2025**, organized by Extracurricular Activities Cell of St. Thomas' College of Engineering and Technology, Kolkata.



Shuvangi Halder, student of **1st Year**, has secured **1st** position in **debate competition**, during the **Induction programme 2024-2025**, organized by Extracurricular Activities Cell of St. Thomas' College of Engineering and Technology, Kolkata.

FACULTY ACHIEVEMENTS:

PUBLICATION DETAILS:

PUBLISHED JOURNAL & CONFERENCE PAPERS

- **Dipankar Kundu**, “**Estimation of Splice Losses between a Photonic Crystal Fiber and a Conventional Single Mode Fiber and between two different Photonic Crystal Fibers without and with Transverse Offset and Angular Misalignment**”, Journal of Optics, 1-14, Sept. 2024.
- **Sumani Mukherjee**, Samhita Dasgupta, Hemanta Naskar, Shreya Firdoushi, Ipshita Bhattacharya, AHM Toufique Ahmed, Deepak Kumar Das, Rajib Bandyopadhyay, Bipan Tudu, “**Development of an easy and economical carbon paste electrode for fisetin detection**”, Innovation and Emerging Technologies, 11, 2450005, July, 2024.



St. Thomas' College of Engineering and Technology

4, D. H. Road, Kidderpore, Kolkata – 700023

Department of Electronics & Communication Engineering

- **P. Pal**, R. Bhattacharya, and J. Poray, "**Segmentation of Pathological Primitive Conditions Based on the Visual Foundation Model**" 13th-14th September, 2024 02 Days ICSSR Conference on "India Towards Viksit Bharat @2047", organized by BVICAM, New Delhi.
- Samhita Dasgupta, AHM Toufique Ahmed, Ipshta Bhattacharjee, Shreya Firdoushi, Don Biswas, **Sumani Mukherjee**, Rajib Bandyopadhyay, Bipan Tudu, "**Crafting a Graphite Electrode with Embedded Y2O3 Nanoparticles for the Electrochemical Detection of Amaranth in Candies**", IEEE Sensors Journal, 24(13), 20750 – 20757, July 2024.



“

“Innovation in Electronics is about making the complex simple, and the invisible visible.”

*Robert Noyce,
Co-Inventor of the Integrated Circuit*

