

St. Thomas' College of Engineering & Technology
Kidderpore, Kolkata, West Bengal

NEWSLETTER



⚡ SPARK ⚡

Department of Electrical Engineering

-: July to Sept.: -

Volume: EE-IX, Issue-I: Oct. 16, 2023

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St. Thomas' College of Engineering and Technology

4, Diamond Harbour Road, Kidderpore, Kolkata- 700023



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NEWSLETTER

Vision

Department:
EE
(NBA Accredited)

To create a strong research-based teaching and learning environment that will cater to the needs of modern Electrical Engineering.

Mission

Editors:
Chitrita Dasgupta
Sampurna Dey
Pausali Saha
Saquib Azad
Surosa Naguri

- To impart knowledge and technology to budding professionals and incite their quest for further enhancement of their learning.
- To produce recognized industry-ready professionals in Electrical Engineering, through educational program incorporating practice and project-based teaching-learning processes.
- To enhance knowledge in Electrical Engineering, through research for sustainable development of the society and the nation as a whole.
- To promote social, environmental and technological responsiveness related to electrical energy through dissemination of knowledge.

Faculty in-charge
Mrs. P. Ghosh,
Assistant Professor,
EE Dept

For further information
& feedback please contact:
poulami.ghosh@stcet.ac.in

Program Educational Objectives (PEO)

- PEO1-** Have skills, in-depth knowledge and proficiency in the core areas of electrical and other related interdisciplinary engineering domains.
- PEO2-** Become successful practitioners in electrical industries and/or to be ready for entrepreneurship keeping in view of the national and global requirements.
- PEO3-** Have ethical values, social commitment and leadership qualities towards application areas of electrical energy.

Departmental Milestones

❖ Technical Workshop


Advisory Committee

Prof. Gautam Banerjee
Director (Admin & Finance), STCET
Dr. Subir Choudhury
Director (Academics), STCET
Prof. (Dr.) Shiba Ghosh
Principal, STCET
Prof. (Dr.) Sujit K. Biswas
Dean (Academics), STCET, Ex. Prof. Jadavpur University

Invited Speakers



Mr. Mainak Das
Chief Cyber Security Architect, EY



Mr. Ankan Karnakar
Senior Consultant, SailPoint Technologies

Program Schedule

Day 1 (16/08/2023, Wednesday)


9:30 am - 10:00 am - Registration
10:00 am - 10:30 am - Inauguration
10:30 am - 10:45 am - Tea Break
10:45 am - 1:00 pm - Session I (Introduction & Live Demonstration of Simulated Attacks)
1:00 pm - 2:00 pm - Lunch
2:00 pm - 4:00 pm - Session II (Practice on Computer (PoC) on Simulated Attacks)
4:00pm - Tea Break

Day 2 (17/08/2023, Thursday)

10:00 am - 11:15 am - Session I (Identity & Access Management (Access Control, Authentication, Authorization))
11:15 am - 11:30 am - Tea Break
11:30 am - 1:00 pm - Session II (Security Information and Event Management, Generative AI and Cyber Security, Metaverse and Cyber Security)
1:00 pm - 2:00 pm - Lunch
2:00 pm - 4:00 pm - Session III (Practice on Computer (PoC) on Password Strengths Assessment Tool)
4:00pm - Tea Break

Day 3 (18/08/2023, Friday)

10:00 am - 11:00 am - Final Assessment
11:00 am - 11:45am - Valedictory Session and Feedback
11:45am - High Tea




A Three Days Workshop
On
**From Signals to Shields & Defending the Digital Realm:
Innovative Cyber Security Session for
Electronics/Electrical Professionals**

August 16, 2023 – August 18, 2023
(Wednesday - Friday)

Organized by
Department of Electrical Engineering*
and
Department of Electronics & Communication Engineering*

In association with
Industry Institute Interaction Cell (IIC)
&
Institution's Innovation Council (IIC)
St. Thomas' College of Engineering & Technology
4, D.H. Road, Kolkata – 700023
*(Approved by AICTE, New Delhi and Affiliated to MAKAUT, WB, *NBA Accredited)*



Conveners
Dr. Juin Acharjee
Assistant Professor, Dept. of ECE, STCET
Mr. Koustuv Sarkar
Assistant Professor, Dept. of EE, STCET



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A three day workshop named “From Signals to Shields & Defending the Digital Realm: Innovative Cyber Security Session for Electronics/ Electrical Professionals” was held on August 16-18, 2023. The Third Year Students of EE Dept. (AY:2023-24) registered in the workshop.

❖ Publication

Prof. (Dr.) Sujit K. Biswas, Dr. Krishna Sarker & Mr. Koustuv Sarkar has published the following journals:

- A. Chakrabarti, **K. Sarkar**, P. R. Kasari, B. Das and **S. K. Biswas**, “A CB-PWM technique for eliminating CMV in multilevel multiphase VSI”, IEEE Transactions on Industrial Electronics, vol. IE-70, no.9, pp 8666 – 8675, Sept 2023.
- S. Das, T. Chakraborty, A. Choudhary, S. Paramanik, **K. Sarkar & K. Sarkar**, “Design of Virtual Electrical Laboratories for Performing Steady State and Transient Response of R-L, R-C and R-L-C Circuit”, i-manager’s Journal on Electronics Engineering, vol. 13, no. 4, July-Sept. 2023.
- Shawanti Roy, Sayan Paramanik, **Krishna Sarker** (2023). An Approach to Harmonic Optimization for 25kVA Auxiliary Converter Used in Indian Railway. i-Manager’s Journal on Electronics Engineering, Volume: 13, Issue: 4, Pages: 36–42, Publication Date: July 1, 2023, ISSN: 2229-7286, DOI: 10.26634/jele.13.4.20155.

❖ Patent

Prof. (Dr.) Sujit K. Biswas has published the following patent:

Aug-2023, "Uninterrupted Power Supply System and Method for Providing Power Flow with Input Power Factor Correction" Patent No. 448544, C. Kundu, S. Das, A. Sadhukhan, **S. K. Biswas** and A. Mitra, Indian Patent, Granted.

***“Genius is one percent inspiration and
ninety-nine percent perspiration.”***

- Thomas Edison